

**Amendment Of Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing Of Claims:**

1. (Currently Amended) A mirror detection signal generator, which generates a mirror detection signal from an RF signal corresponding the reflected light from a recording medium, comprises the following parts:
  - a first peak-hold circuit that holds the bottom level of said RF signal [at] and having a first [attenuation] droop rate and outputs a bottom-hold signal;
  - a second peak-hold circuit that holds the top level of said RF signal [at] and having a second [attenuation] droop rate and outputs a first envelope signal;
  - a third peak-hold circuit that holds the bottom level of said RF signal [at] and having a third [attenuation] droop rate and outputs a second envelope signal;
  - a first reference signal generator that outputs the first reference signal generated on the basis of said bottom-hold signal and said first envelope signal;
  - [and] a first comparator that compares said first reference signal with said second envelope signal and generates said mirror detection signal[.];
  - a fourth peak-hold circuit that holds the top level of said RF signal and having a fourth droop rate and outputs a top-hold signal;
  - a second reference signal generator that outputs the second reference signal generated on the basis of said top-hold signal and said bottom-hold signal;
  - and a second comparator that compares said second reference signal and said first envelope signal and generates a defect detection signal, said first droop rate being controlled by said defect detection signal.
  
2. (Previously Presented) The mirror detection signal generator described in Claim 1,
  - said first reference signal generator comprises a first voltage divider that divides the voltage of said bottom-hold signal and said first envelope signal.

3. (Previously Presented) The mirror detection signal generator described in Claim 2,

said first reference signal generator comprises a first amplifier that amplifies said divided voltage at an amplification rate corresponding to the type of recording medium.

4. (Previously Presented) The mirror detection signal generator described in Claim 3,

said first reference signal generator comprises an offset circuit that adds a prescribed offset voltage to the output signal of said first amplifier.

5. (Previously Presented) The mirror detection signal generator described in Claim 1, further comprising

a filter that performs a prescribed signal processing for said second envelope signal, and

a second amplifier that amplifies said second envelope signal at an amplification rate corresponding to the type of recording medium.

6. (Cancelled)

7. (Currently Amended) The mirror detection signal generator described in Claim [6.] 1;

said second reference signal generator comprises a second voltage divider that divides the voltage of said top-hold signal and said bottom-hold signal.

8. (Previously Presented) The mirror detection signal generator described in Claim 7, wherein

said second reference signal generator comprises a third amplifier that amplifies the divided voltage output from said second voltage divider at an amplification rate corresponding to the type of recording medium.

9. (Previously Presented) The mirror detection signal generator described in Claim 7 further comprising

a fourth amplifier that amplifies said first envelope signal at an amplification rate corresponding to the type of recording medium.

10. (Currently Amended) The mirror detection signal generator described in Claim [6,] 1:  
said first attenuation rate is increased when said defect detection signal is output.

11. (Previously Presented) The mirror detection signal generator described in Claim 2, further comprising  
a filter that performs a prescribed signal processing for said second envelope signal, and  
a second amplifier that amplifies said second envelope signal at an amplification rate corresponding to the type of recording medium.

12. (Previously Presented) The mirror detection signal generator described in Claim 3, further comprising  
a filter that performs a prescribed signal processing for said second envelope signal, and  
a second amplifier that amplifies said second envelope signal at an amplification rate corresponding to the type of recording medium.

13. (Previously Presented) The mirror detection signal generator described in Claim 4, further comprising  
a filter that performs a prescribed signal processing for said second envelope signal, and  
a second amplifier that amplifies said second envelope signal at an amplification rate corresponding to the type of recording medium.

14. (Cancelled)

15. (Cancelled)

16. (Cancelled)

17. (Cancelled)

18. (Cancelled)

19. (Cancelled)

20. (Cancelled)